

REMARKS

Claims 8 and 10-14 are presently pending and stand rejected. Claims 1-7 and 9 are cancelled without prejudice.

Claims 8 and 10-14 were rejected under 35 U.S.C. 102(b) as anticipated by Choi, U.S. Patent 5,835,150. Claim 8 is amended to recite, among other limitations, "a line address computer for calculating a starting address for a row of luma pixels and calculating a starting address for a row of chroma pixels". Examiner has indicated that in Choi "the controller 24 inherently includes a line address computer for addressing or calculating the starting address of video data, which includes luminance data and chrominance data. Conventionally, luminance data and chrominance data are stored in separate areas of a memory. In order to access ... these data, separate addresses are needed." Office Action, at 2.

"In relying upon the theory of inherency, the examiner must provide a basis in fact and/or technical reasoning to reasonably support the determination that the allegedly inherent characteristic necessarily flows from the teachings of the applied prior art." *Ex parte Levy*, 17 USPQ2d 1461, 1464 (Bd. Pat. App. & Inter. 1990) (emphasis in original)." MPEP 2112.

Examiner basis the conclusion that "controller 24 inherently includes a line address computer" on the fact that "Conventionally, luminance data and chrominance data are stored in separate areas of a memory". However, "Conventionally" falls short of the requirement that the "inherent characteristic necessarily flows". Thus, Assignee respectfully traverses Examiner's conclusion that the limitations of claim 9 are necessarily present in Choi.

With respect to claim 10, even if "controller 24

includes a line address computer for addressing or calculating the starting address of video data, which include luminance and chrominance data" (Emphasis Added), it is not necessarily the case that the "line address computer calculates a starting address for another row of chroma pixels, wherein the another row of chroma pixels is adjacent to the row of chroma pixels". Accordingly, Assignee respectfully traverses the rejection to claim 10 and request that Examiner withdraw it.

With respect to claim 14, Assignee would like to call Examiner's attention to the recited limitation "wherein the line address computer interpolates a chroma row from the row of chroma pixels and the another row of chroma pixels". Assignee respectfully submits that the fact that "Conventionally, luminance data and chrominance data are stored in separate areas of a memory. In order to access ... these data, separate addresses are needed" does not establish that the "line address computer interpolates a chroma row from the row of chroma pixels and the another row of chroma pixels". Accordingly, Assignee respectfully traverses the rejection to claim 14 and request that Examiner withdraw it.

The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

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Respectfully submitted,



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